

### ***SPECIFICATION AND DRAWINGS***

#### ***In the Specification:***

Please replace the last paragraph on page 10 of the specification with the following:

In decision block 42 it is determined whether or not normal or active mode should be resumed. In the case of a computer, entry into active mode may be signified for example by detecting the user striking a key or moving a mouse or other input device connected to the computer. During the sleep mode errors may have been introduced into the DRAM memory due to the reduce refresh rate. Upon resuming normal mode the error correction decoding circuit 5 (Fig. 1) uses, in block 44, the stored ECC bits to correct any error to the critical bits that may have occurred during the sleep mode. Thereafter, the ECC bits are discarded at block 66 and normal refresh cycles are resumed.